



**UNIVERSITAS PANCASILA**  
**PROFIL LULUSAN DAN CAPAIAN PEMBELAJARAN**



**UNIVERSITAS PANCASILA**  
**PROFILE OF THE GRADUATE AND LEARNING OUTCOMES**

<p><b>Fakultas : Teknik</b> <b>Program Studi : Elektro</b> <b>Jenjang : Sarjana (S-1)</b></p>	<p><b>Faculty : Engineering</b> <b>Study Program : Electrical Engineering</b> <b>Study level : Bachelor (Strata-1)</b></p>
<p><b>I. PROFIL LULUSAN</b> Lulusan Program Studi Teknik Elektro Universitas Pancasila memiliki Profil Lulusan sebagai berikut:</p> <ol style="list-style-type: none"> <li>1. Sarjana Teknik Elektro yang mampu merancang solusi perangkat keras dan lunak bidang teknik elektro secara etis dan berakhlak mulia berlandaskan nilai-nilai Pancasila.</li> <li>2. Sarjana Teknik Elektro yang profesional mampu berkomunikasi, berkolaborasi dalam tim, berpikir inovatif dan adaptif terhadap dinamika dunia kerja.</li> <li>3. Sarjana Teknik Elektro yang mampu merekayasa sistem kendali terdistribusi berbasis komunikasi data real-time, mengintegrasikan jaringan sensor nirkabel dengan aktuator industri, serta menerapkan prinsip Industrial IIoT untuk menciptakan pabrik cerdas (<i>smart factory</i>) yang aman dan berkelanjutan.</li> </ol> <p><b>II. CAPAIAN PEMBELAJARAN</b></p> <ol style="list-style-type: none"> <li>1. CPL-1 Membentuk insan yang beriman, bertakwa, berakhlak mulia, dan berkarakter sesuai dengan nilai-nilai Pancasila, serta menjunjung tinggi norma dan etika akademik.</li> <li>2. CPL-2 Membentuk Insan yang berperan aktif membangun peradaban melalui kontribusi nyata bagi bangsa, menjunjung tinggi hukum, keanekaragaman budaya, tanggung jawab profesi dan semangat kewirausahaan dalam setiap interaksi social-profesional.</li> </ol>	<p><b>I. GRADUATE PROFILE</b> The graduate of the Electrical Engineering Department of Universitas Pancasila meets the following graduate profile:</p> <ol style="list-style-type: none"> <li>1. An Electrical Engineering graduate equipped to architect and implement hardware and software solutions, upholding rigorous professional ethics and exemplary moral integrity anchored in the values of Pancasila..</li> <li>2. A forward-thinking Electrical Engineering professional demonstrating excellence in strategic communication, multidisciplinary collaboration, and innovative problem-solving, possessing the agility to thrive within a rapidly evolving industrial landscape.</li> <li>3. A Bachelor of Electrical Engineering demonstrating advanced mastery in engineering distributed control systems via real-time data communication, integrating wireless sensor networks with industrial actuators, and leveraging Industrial IIoT frameworks to pioneer secure and sustainable smart manufacturing ecosystems.</li> </ol> <p><b>II. LEARNING OUTCOMES</b></p> <ol style="list-style-type: none"> <li>1. CPL-1 To cultivate graduates who are God-fearing, possess noble character, and embody the values of Pancasila, while upholding academic norms and ethics.</li> <li>2. CPL-2 To develop individuals who play an active role in advancing civilization through tangible contributions to the nation, upholding legal frameworks,</li> </ol>



**UNIVERSITAS PANCASILA**  
**PROFIL LULUSAN DAN CAPAIAN PEMBELAJARAN**



**UNIVERSITAS PANCASILA**  
**PROFILE OF THE GRADUATE AND LEARNING OUTCOMES**

<p>3. CPL-3 Menguasai fondasi teoritis multidisipliner (matematika, fisika, prinsip rangkaian listrik AC/DC, dan elektromagnetika) untuk pemodelan sistem elektro, dilengkapi kompetensi analisis performa sistem melalui instrumentasi presisi dan interpretasi data berbasis standar teknis.</p> <p>4. CPL-4 Menginternalisasi metodologi perancangan sistem terintegrasi (<i>hardware/software</i>) dan analisis perilaku rangkaian elektronika analog/digital, diperkaya dengan perkembangan mutakhir IoT/Kecerdasan Buatan untuk inovasi solusi elektro yang kreatif, bertanggung jawab, dan terimplementasi dari konsep hingga validasi prototipe.</p> <p>5. CPL-5 Menguasai kerangka teoritis etika rekayasa (<i>engineering ethics</i>) dan standar industri global (IEEE/IEC/ISO) sebagai dasar pengambilan keputusan teknis berkelanjutan, menjamin solusi elektro yang beretika, aman, dan relevan dengan fenomena teknik kontemporer.</p> <p>6. CPL-6 Memiliki kemampuan berkomunikasi secara efektif, baik lisan maupun tulisan, bekerja sama dalam tim, dan berinovasi, serta mampu beradaptasi dalam lingkungan pekerjaan dan masyarakat.</p> <p>7. CPL-7 Mampu berpikir logis, kritis, sistematis, dan inovatif dalam mengembangkan atau menerapkan teknologi berbasis nilai humaniora sesuai bidang keahlian Teknik Elektro, serta menunjukkan kinerja yang</p>	<p>cultural diversity, professional accountability, and an entrepreneurial spirit in every social and professional interaction.</p> <p>3. CPL-3 To master multidisciplinary theoretical foundations (mathematics, physics, AC/DC circuit principles, and electromagnetics) for electrical system modeling, complemented by competencies in system performance analysis through precision instrumentation and data interpretation based on technical standards.</p> <p>4. CPL-4 To internalize integrated system design methodologies (<i>hardware/software</i>) and behavioral analysis of analog/digital electronic circuits, enriched by cutting-edge developments in IoT and Artificial Intelligence to produce creative, responsible, and implementable electrical solutions from concept to prototype validation.</p> <p>5. CPL-5 To master the theoretical framework of engineering ethics and global industry standards (IEEE/IEC/ISO) as a basis for sustainable technical decision-making, ensuring electrical solutions that are ethical, safe, and relevant to contemporary engineering phenomena.</p> <p>6. CPL-6 To possess the ability to communicate effectively, both orally and in writing, collaborate within teams, and innovate while demonstrating adaptability within professional environments and society.</p>
--	--



**UNIVERSITAS PANCASILA**  
**PROFIL LULUSAN DAN CAPAIAN PEMBELAJARAN**



**UNIVERSITAS PANCASILA**  
**PROFILE OF THE GRADUATE AND LEARNING OUTCOMES**

<p>mandiri, bermutu, dan terukur dalam menyelesaikan tugas, termasuk menyusun laporan ilmiah tugas akhir secara terstruktur dengan menjunjung tinggi etika akademik.</p> <p>8. CPL-8 Mampu membangun jejaring kerja yang kolaboratif dan proaktif lintas lembaga, bertanggung jawab atas hasil kerja individu maupun tim, serta mendokumentasikan, mengelola, dan mengamankan data secara sistematis guna menjamin integritas akademik dan mencegah <i>plagiarism</i>.</p> <p>9. CPL-9 Mampu merancang dan mengimplementasikan solusi perangkat keras dan lunak berbasis data, termasuk sistem kendali mikro dan sistem terintegrasi, untuk menyelesaikan permasalahan umum dan spesifik di bidang Teknik Elektro secara presisi dan holistik.</p> <p>10. CPL-10 Mampu merancang dan membangun infrastruktur jaringan komunikasi dan informasi, baik kabel maupun nirkabel, yang andal, skalabel, aman, dan sesuai dengan perkembangan standar teknis terkini.</p> <p>11. CPL-11 Mampu mengembangkan sistem pengendalian industri berbasis data dan kecerdasan buatan yang efisien, adaptif, serta interoperabel, dengan mempertimbangkan kemudahan pemeliharaan dan dinamika lingkungan produksi.</p>	<p>7. CPL-7 To demonstrate logical, critical, systematic, and innovative thinking in developing or applying technology based on humanities values within the field of electrical engineering; and to exhibit independent, high-quality, and measurable performance in completing tasks, including the structured preparation of final project scientific reports while upholding academic integrity.</p> <p>8. CPL-8 To build collaborative and proactive networks across institutions, take responsibility for individual and team outcomes, and systematically document, manage, and secure data to ensure academic integrity and prevent plagiarism.</p> <p>9. CPL-9 To design and implement data-driven hardware and software solutions, including micro-control systems and integrated systems, to resolve general and specific problems in the field of electrical engineering with precision and a holistic approach.</p> <p>10. CPL-10 To design and construct communication and information network infrastructures, both wired and wireless, that are reliable, scalable, secure, and compliant with the latest technical standards.</p> <p>11. CPL-11 To develop industrial control systems based on data and artificial intelligence that are efficient, adaptive, and</p>
--	--



**UNIVERSITAS PANCASILA  
PROFIL LULUSAN DAN CAPAIAN PEMBELAJARAN**



**UNIVERSITAS PANCASILA  
PROFILE OF THE GRADUATE AND LEARNING OUTCOMES**

<p>12. CPL-12 Mampu merancang solusi rekayasa Teknik Elektro yang berkelanjutan dengan mengintegrasikan nilai-nilai etika profesi, keselamatan operasional, dan keberlanjutan lingkungan.</p>	<p>interoperable, while considering ease of maintenance and the dynamics of production environments.</p> <p>12. CPL-12 To design sustainable electrical engineering solutions by integrating professional ethical values, operational safety, and environmental sustainability.</p>
---	---

Jakarta, 27 April 2026

Disetujui / Approval,

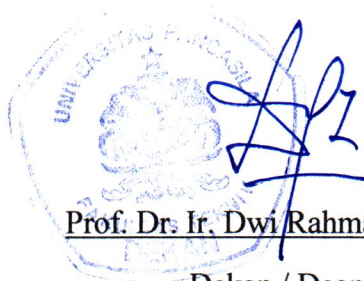
Nur Yulianti Hidayah ST.,MT  
Ketua Satuan Jaminan Mutu  
/ Head of Education Quality Assurance

Dibuat / Prepared,



Dr. Ane Prasetyowati, ST.,MT  
Ketua Program Studi / Head of Department

Disahkan/Ratified,



Prof. Dr. Ir. Dwi Rahmalina, MT  
Dekan / Dean